







- [3] Manolis Katevenis; Stefanos Sidiropoulos; Costas Courcoubetis, "Weighted round-robin cell multiplexing in a general-purpose ATM switch chip", IEEE Journal on Selected Areas in Communications, (Vol. 9, Issue:8), October 1991
- [4] H.S. Behera; Simpi Patel; Bijayalakshmi Panda, "A New Dynamic Round Robin and SRTN Algorithm with Variable Original Time Slice and Intelligent Time Slice for Soft Real Time Systems", International Journal of Computer Applications (0975 – 8887) Volume 16– No.1, February 2011
- [5] Xin Chen; Xudong Xiang; Jianxiong Wan, "A Software Implementation of AFDX End System", 2009 International Conference on New Trends in Information and Service Science
- [6] A. Mifdaoui, F. Frances, C. Fraboul, "Real-Time Communication over Switched Ethernet for Military Applications" In Proceedings of the conference on Emerging network experiment and technology, pp. 45-56, ACM, 2005.
- [7] AEEC; Aeronautical Radio Inc.; "Aircraft Data Network Part 7 Avionics Full Duplex Switched Ethernet Network (ARINC SPECIFICATION 664 P7-1)", September 23, 2009
- [8] Jonathan Woodruff, "Deficit Round Robin Input Arbiter for NetFPGA"
- [9] M. Shreedhar and G. Varghese. Efficient fair queuing using deficit round-robin. IEEE/ACM Transactions on Networking (TON), 4(3):385, 1996.